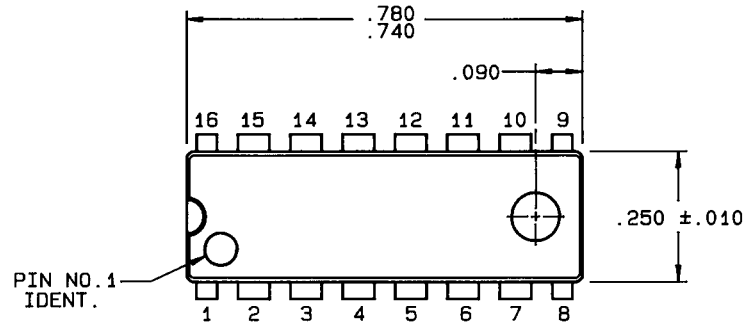
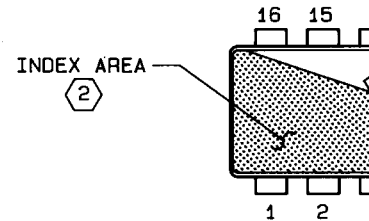


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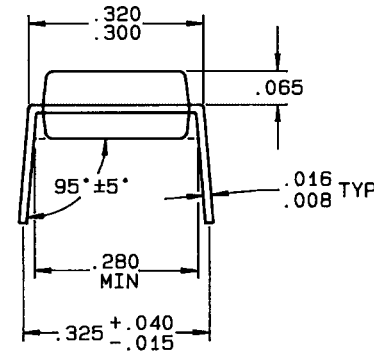
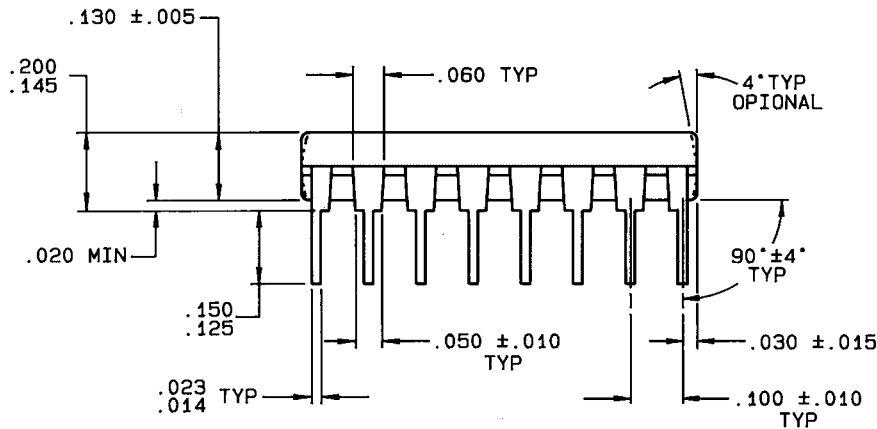
REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
E	LEAD SPREAD WAS: .325+/.025/- .015 REDRAWN	01696	06-26-84	SDK/TM
F	REVISE NOTES AND DIMS IN TOP, FRONT AND SIDE VIEWS; ADD INDEX AREA TO OPT 2; REDRAW /CAD.	06217	12-1-88	MS/MS



OPTION 01



OPTION 02



NOTES: UNLESS OTHERWISE SPECIFIED

- STANDARD LEAD FINISH:
300 MICROINCHES MINIMUM TIN/LEAD 85/15 OR
200 MICROINCHES MINIMUM TIN/LEAD 60/40 OR 63/37
ON ALLOY 42, COPPER OR EQUIVALENT.

- INDEX AREA: A NOTCH OR LEAD #1 IDENTIFICATION MARK SHALL BE LOCATED ADJACENT TO LEAD #1 AND WITHIN THE SHADED AREA AS SHOWN.

APPROVALS		DATE	NATIONAL SEMICONDUCTOR CORPORATION			
DRAWN M. SUCHY		12-01-88	2900 Semiconductor Drive, Santa Clara, CA 95052-8090			
DESIGN. CHK <i>Michael J. ...</i>		12/19/88	MOLDED DIP (N), 16 LEAD			
ENGR. CHK <i>John A. ...</i>		12/14/88				
APPROVAL		/ /	SCALE N/A	SIZE B	DRAWING NUMBER MKT-N16E	REV F
 PROJECTION INCH MM			FORMERLY:	N/A	SHEET 1 OF 1	